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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/841,845	04/25/2001	Tadashi Ezaki	450100-03180	5834

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EXAMINER

VENT, JAMIE J

ART UNIT	PAPER NUMBER
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2621

DATE MAILED: 07/31/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/841,845

Applicant(s)

EZAKI, TADASHI

Examiner

Jamie Vent

Art Unit

2621

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 10 May 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-45 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-45 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Arguments

Applicant's arguments filed May 10, 2006 have been fully considered but they are not persuasive.

On pages 13-17 applicant argues that Ezaki in view of Ueno fails to disclose or suggest the following limitation: "selecting a parameter associated with an error check code based on the detected type of additional information" as recited in independent claim 1. Ezaki discloses in Column 5 Lines 50+ the generating an error check code on the basis of selected parameter wherein the error check provides proper transmission of the signal. Furthermore as described in Column 1 Lines 33-48 through the addition of error correcting codes prevents errors along the transmission lines and further identifies further errors. Although, the examiner understands all of applicants points the examiner can not agree. Therefore, the rejection is maintained.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-45 rejected under 35 U.S.C. 103(a) as being unpatentable by Ezaki et al (US 5,822,425) in view of Ueno (US 5,710,771).

[claims 1 & 9]

In regard to Claims 1 and 9, Ezaki et al discloses a signal transmission method and apparatus for transmitting a signal including main information and various types of additional information added to said main information, comprising the steps of:

- detecting the type of additional information to be added (Figure 10b the decoder detects the time of additional information to be added as further described in Column 7 Lines 28-44);
- generating an error check code on the basis of said selected parameter (Column 5 Lines 50+ describes generating an error check code on the basis of selected parameter); and
- inserting the additional information with said error check code into main information and transmitting a resultant signal (Column 5 Lines 50+ describes the end error code being inserted into the main information); however, fails to disclose parameter associated with an error check code depending upon the detected type of the additional information.

Ueno discloses a multichannel communication system wherein main information and additional information is detected through error codes. The error codes are associated with the additional information as disclosed in Column 1 Lines 34+ through Column 2 Lines 1-30. The selection of a parameter associated with the error codes provides the system the ability to receive proper transmission of the main information as well as additional information. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to use the signal transmission method, as disclosed by Ezaki et al, and further incorporate a transmission method which selects parameters

with an error check code that is dependent on additional information that is being transmitted, as disclosed by Ueno.

[claims 2 & 10]

In regard to Claims 2 and 10, Ezaki et al discloses a signal transmission method and apparatus wherein said main information is a vertical blanking interval (VBI) signal of a video signal (Figure 1 shows the VBI as further described in Column 1 Lines 34+ and Column 4 Lines 45-61).

[claims 3 & 11]

In regard to Claims 3 and 11, Ezaki et al discloses a signal transmission method and apparatus wherein said additional information added to the main information includes copy management information (Column 7 Lines 27+ describes the copy management information that is available in the system).

[claims 4 & 12]

In regard to Claims 4 and 12, Ezaki et al discloses a signal transmission method and apparatus wherein in said detection steps the type of additional information is detected on the basis of bit assignment within a predetermined bit range of the additional information (Column 6 Lines 50+ through Column 6 Lines 1-15 describes the detection of additional information that is detected on the basis of bit assignments through a predetermined bit range).

[claims 5 & 13]

In regard to Claims 5 and 13, Ezaki et al discloses a signal transmission method and apparatus wherein said error check code is a CRCC (Cyclic Redundancy Check Code)

(Figure 2 shows the CRCC error check code which is further explained in Column 1 Lines 47-62).

[claims 6 & 14]

In regard to Claims 6 and 14, Ezaki et al discloses a signal transmission method and apparatus wherein when the additional information is of a predetermined type, said selection step selects a parameter which is common among two or more signal transmission methods (Column 7 Lines 27-43 describes the additional information that is added and furthermore since the selection parameters are available through the VBI which is common among various transmission methods and thereby meets the limitation)

[claims 7 & 15]

In regard to Claims 7 and 15, Ezaki et al discloses a signal transmission method and apparatus wherein said parameter associated with the error check code is an initial value used in the generation of the error check code (Figure 2 shows the error check code being a 6 bit CRCC code section wherein the initial value is used for the generation of an error as further described Column 5 Lines 50+).

[claims 8 & 16]

In regard to Claims 8 and 16, Ezaki et al discloses a signal transmission method and apparatus wherein said parameter associated with the error check code is a formula for generating the error check code or is a shift register configuration implementing said formula (Column 6 Lines 15+ describes the transmission of the parameters associated

with the error check code and the generation of the error check code by using the transmitted characters and comparing the codes to check the sum).

[claims 17 & 25]

In regard to Claims 17 and 25, Ezaki et al discloses a signal receiving method and apparatus for receiving main information including additional information with an error check code added to said main information, comprising the steps of:

- receiving a signal (Figure 10b shows the receiving of the signal);
- extracting additional information with an error check code from the received signal (Figure 10b shows the extraction of additional information from the demodulator to the error processing circuit as further described in Column 7 Lines 15-26);
- detecting the type of said additional information (Figure 10b the decoder detects the time of additional information to be added as further described in Column 7 Lines 28-44);
- selecting a parameter associated with the error check code depending upon the detected type of the additional information (Column 5 Lines 50+ describes generating an error check code on the basis of selected parameter); and
- checking the additional information using the error check code on the basis of said selected parameter (Column 6 Lines 15-43 describes the checking of the additional information using the error check code).

[claims 18 & 26]

Art Unit: 2621

In regard to Claims 18 and 26, Ezaki et al discloses a signal receiving method and apparatus wherein said main information is a vertical blanking interval (VBI) signal of a video signal (Figure 1 shows the VBI as further described in Column 1 Lines 34+ and Column 4 Lines 45-61).

[claims 19 & 27]

In regard to Claims 19 and 27, Ezaki et al discloses a signal receiving method and apparatus wherein said additional information added to the main information includes copy management information (Column 7 Lines 27+ describes the copy management information that is available in the system).

[claims 20 & 28]

In regard to Claims 20 and 28, Ezaki et al discloses a receiving method and apparatus wherein in said detection step, the type of the additional information is detected on the basis of bit assignment within a predetermined bit range of the additional information (Figure 4 shows the additional information and the CGMS which are included in the bit stream and thereby used for detecting the additional information and copy control information through the bit range as further described in Column 5 Lines 50+).

[claims 21 & 29]

In regard to Claims 21 and 29, Ezaki et al discloses a signal receiving method and apparatus wherein said error check code is a CRCC (Cyclic Redundancy Check Code) (Figure 2 shows the CRCC error check code which is further explained in Column 1 Lines 47-62).

[claims 22 & 30]

Art Unit: 2621

In regard to Claims 22 & 30, Ezaki et al discloses a signal receiving method and apparatus wherein when the additional information is of a predetermined type, said selection step selects a parameter which is common among two or more signal transmission methods (Column 7 Lines 27-43 describes the additional information that is added and furthermore since the selection parameters are available through the VBI which is common among various transmission methods and thereby meets the limitation).

[claims 23 & 31]

In regard to Claims 23 and 31, Ezaki et al discloses a signal receiving method and apparatus wherein said parameter associated with the error check code is an initial value used in generation of the error check code (Figure 2 shows the error check code being a 6 bit CRCC code section wherein the initial value is used for the generation of an error as further described Column 5 Lines 50+).

[claims 24 & 32]

In regard to Claims 24 and 32, Ezaki et al discloses a signal receiving method and apparatus wherein said parameter associated with the error check code is a formula for generating the error check code or is a shift register configuration implementing said formula (Figure 2 shows the error check code being a 6 bit CRCC code section wherein the initial value is used for the generation of an error as further described Column 5 Lines 50+).

[claim 33]

In regard to Claim 33, Ezaki et al discloses a VBI signal generating apparatus, as previously disclosed in Claim 1, with the additional limitations of:

- timing detector for detecting the timing of inserting a VBI signal into the video signal (Figure 17 shows a line counter 85 which counts the number of horizontal synchronous signal is received and thereby allows for proper inserting of the VBI as further described in Column 12 Lines 60+ through Column 13 Lines 1-5);
- a VBI signal generator for generating, in response to timing detected with said timing detector, a VBI signal including additional information with an error check code, wherein said error check code generator switches a parameter used in generation of the error check code depending upon the type of the additional information (Column 13 Lines 1-31 describes the generation of the gate pulse which corresponds to the horizontal synchronous signal and thereby generates the error check code).

[claim 34]

In regard to Claim 34, Ezaki et al discloses a video signal transmitting apparatus as previously disclosed in Claim 33 with the additional limitations:

- a replacing unit for, in response to a timing detected by said timing detector, inserting the generated VBI signal into a video signal (Column 14 Lines 30-43 describes the replacing unit for inserting the VBI signal which was generated); and

- a signal distributing unit for distributing the video signal including the VBI signal inserted therein, wherein said error check code generator switches a parameter used in generation of the error check code depending upon the type of the additional information (Column 13 Lines 44-54 describes the distributing of a signal wherein the VBI is inserted).

[claim 35]

In regard to Claim 35, Ezaki et al discloses a video signal processing apparatus for processing a video signal as disclosed in Claim 34, with the additional limitations:

- a processing unit for processing the video signal, wherein said error check code generator switches a parameter used in generation of the error check code depending upon the type of the additional information (Column 5 Lines 50+ describes the parameter associated with an error check code that is detected type of additional information).

[claims 36 & 37]

In regard to Claims 36 and 37, Ezaki et al discloses a video signal receiving apparatus for receiving a video signal including additional information with an error detection, comprising:

- a receiving unit for receiving the video signal (Figure 10b shows the receiving of the signal);
- a timing detector for detecting the timing of extracting the additional information from the video signal (Figure 17 shows a line counter 85 which counts the number of horizontal synchronous signal is received and

thereby allows for proper inserting of the VBI as further described in Column 12 Lines 60+ through Column 13 Lines 1-5);

- an extraction unit for, in response to the timing detected by said timing detector, extracting the additional information from the video signal (Figure 10b shows the extraction of additional information from the demodulator to the error processing circuit as further described in Column 7 Lines 15-26);
- an error checking unit for checking the additional information using the error check code included in the additional information (Column 7 Lines 50+ describes the error check code generator that is used to generate an error check code); and;
- a decoding unit for decoding the additional information depending upon the result of error checking (Figure 18 describes a decoding unit as further described in Column 13 Lines 33-44); and
- a display unit for displaying the video signal on a screen in accordance with the additional information (Figure 15 shows the output of the stream to a display unit which has been processes with additional information).

[claim 38]

In regard to Claim 38, Ezaki et al discloses a video signal processing apparatus for processing a video signal including additional information with an error check code, comprising:

- an input unit for inputting a video signal (Figure 15 shows an input for inputting video);

- a timing detector for detecting the timing of extracting the additional information from the video signal (Figure 17 shows a line counter 85 which counts the number of horizontal synchronous signal is received and thereby allows for proper inserting of the VBI as further described in Column 12 Lines 60+ through Column 13 Lines 1-5);
- an extraction unit for, in response to the timing detected by said timing detectors extracting the additional information from the video signal (Figure 10b shows the extraction of additional information from the demodulator to the error processing circuit as further described in Column 7 Lines 15-26);
- an error checking unit for checking the additional information using the error check code included in the additional information (Column 7 Lines 50+ describes the error check code generator that is used to generate an error check code);
- a decoding unit for decoding the additional information depending upon the result of error checking (Figure 18 describes a decoding unit as further described in Column 13 Lines 33-44); and
- a processing unit for processing the video signal in accordance with the additional information, wherein said error checking unit switches a parameter used in the error checking depending upon the type of the additional information (Column 18 shows a processing unit for processing

the video signal wherein the error checking unit switches the type of additional information as it is sent back to the register).

[claim 39]

In regard to Claim 39, Ezaki et al discloses a recording medium for recording a video signal, wherein said video signal includes a VBI signal inserted therein, said VBI signal including additional information with an error check code generated by applying a parameter depending upon the type of said additional information (Column 13 Lines 1-31 describes the generation of the gate pulse which corresponds to the horizontal synchronous signal and thereby generates the error check code)..

[claim 40]

In regard to Claim 40, Ezaki et al discloses a recording medium wherein said additional information includes copy management information (Column 7 Lines 27+ describes the copy management information that is available in the system).

[claim 41]

In regard to Claim 41, Ezaki et al discloses a recording medium wherein the type of additional information is determined on the basis of bit assignment within a predetermined bit range of the additional information (Column 6 Lines 50+ through Column 6 Lines 1-15 describes the detection of additional information that is detected on the basis of bit assignments through a predetermined bit range).

[claim 42]

In regard to Claim 42, Ezaki et al discloses a recording medium wherein said error check code is a CRCC (Cyclic Redundancy Check Code) (Figure 2 shows the CRCC error check code which is further explained in Column 1 Lines 47-62).

[claim 43]

In regard to Claim 43, Ezaki et al discloses a recording medium wherein information is of a predetermined type, the error check code is generated by applying a parameter which is common among two or more signal transmission methods (Column 7 Lines 27-43 describes the additional information that is added and furthermore since the selection parameters are available through the VBI which is common among various transmission methods and thereby meets the limitation).

[claim 44]

In regard to Claim 44, Ezaki et al discloses a recording medium wherein said parameter associated with the error check code is an initial value used in the generation of the error check code (Figure 2 shows the error check code being a 6 bit CRCC code section wherein the initial value is used for the generation of an error as further described Column 5 Lines 50+).

[claim 45]

In regard to Claim 45, Ezaki et al discloses a recording medium wherein said parameter associated with the error check code is a formula for generating the error check code or is a shift register configuration implementing said formula (Figure 2 shows the error check code being a 6 bit CRCC code section wherein the initial value is used for the generation of an error as further described Column 5 Lines 50+).

Conclusion

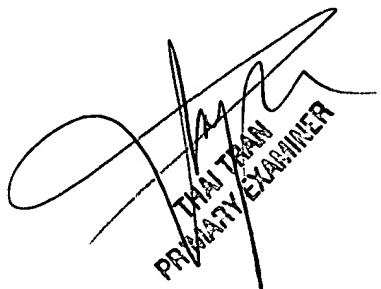
THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jamie Vent whose telephone number is 571-272-7384. The examiner can normally be reached on 7:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thai Tran can be reached on 571-272-7382. Effective July 15, 2005, the Central Fax Number will change to 571-273-8300. Faxes sent to the old number (703-872-9306) will be routed to the new number until September 15, 2005.


THAI TRAN
PRIMARY EXAMINER